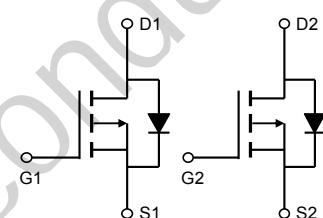
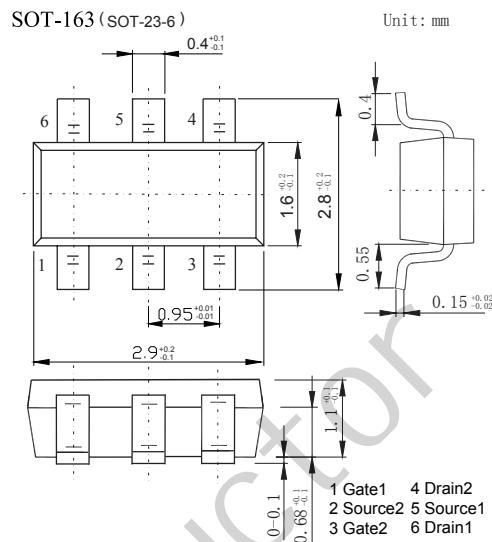


FEATURE

- ◆ -20V/-3.0A, $R_{DS(ON)}=90m\Omega$ (typ.)@ $V_{GS}=4.5V$
- ◆ -20V/-2.2A, $R_{DS(ON)}=110m\Omega$ (typ.)@ $V_{GS}=2.5V$
- ◆ Super high design for extremely low $R_{DS(ON)}$
- ◆ Exceptional on-resistance and Maximum DC current capability
- ◆ Full RoHS compliance
- ◆ SOT-23-6L package design

APPLICATIONS

- ◆ Power Management
- ◆ Portable Equipment
- ◆ DC/DC Converter
- ◆ Load Switch
- ◆ DSC
- ◆ LCD Display inverter


Absolute Maximum Ratings T_A=25°C unless otherwise noted

Symbol	Parameter	Ratings	Units
V _{DSS}	Drain-Source Voltage	-20	V
V _{GSS}	Gate-Source Voltage	±12	V
I _D	Drain Current – Continuous	-3.0	A
	– Pulsed	-8	
P _D	Power Dissipation for Single Operation	1.1	W
		0.96	
		0.8	
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 to +150	°C

Thermal Characteristics

R _{θJA}	Thermal Resistance, Junction-to-Ambient	130	°C/W
R _{θJC}	Thermal Resistance, Junction-to-Case	60	°C/W

Electrical Characteristics

 $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain–Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-20			V
ΔBV_{DSS} ΔT_J	Breakdown Voltage Temperature Coefficient	$I_D = -250 \mu\text{A}$, Referenced to 25°C		-11		mV/°C
I_{BS}	Zero Gate Voltage Drain Current	$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}$			-1	μA
I_{GSSF}	Gate–Body Leakage, Forward	$V_{GS} = 12 \text{ V}, V_{DS} = 0 \text{ V}$			100	nA
I_{GSSR}	Gate–Body Leakage, Reverse	$V_{GS} = -12 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$	-0.6	-1.0	-1.5	V
$\Delta V_{GS(th)}$ ΔT_J	Gate Threshold Voltage Temperature Coefficient	$I_D = -250 \mu\text{A}$, Referenced to 25°C		3		mV/°C
$R_{DS(on)}$	Static Drain–Source On–Resistance	$V_{GS} = -4.5 \text{ V}, I_D = -3.0 \text{ A}$ $V_{GS} = -2.5 \text{ V}, I_D = -2.2 \text{ A}$ $V_{GS} = -4.5 \text{ V}, I_D = -3.0 \text{ A}, T_J = 125^\circ\text{C}$		90 110 127	105 153 155	$\text{m}\Omega$
$I_{D(on)}$	On–State Drain Current	$V_{GS} = -4.5 \text{ V}, V_{DS} = -5 \text{ V}$	-6			A
g_{FS}	Forward Transconductance	$V_{DS} = -5 \text{ V}, I_D = -3.5 \text{ A}$		6		S
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1.0 \text{ MHz}$		437		pF
C_{oss}	Output Capacitance			88		pF
C_{rss}	Reverse Transfer Capacitance			51		pF
Switching Characteristics						
$t_{d(on)}$	Turn–On Delay Time	$V_{DD} = -10 \text{ V}, I_D = -1 \text{ A}, R_{GEN} = 6 \Omega$		9	18	ns
t_r	Turn–On Rise Time			12	22	ns
$t_{d(off)}$	Turn–Off Delay Time			10	20	ns
t_f	Turn–Off Fall Time			5	10	ns
Q_g	Total Gate Charge	$V_{DS} = -10 \text{ V}, I_D = -3.0 \text{ A}, V_{GS} = -4.5 \text{ V}$		3.7	5.2	nC
Q_{gs}	Gate–Source Charge			0.65		nC
Q_{gd}	Gate–Drain Charge			1.3		nC
Drain–Source Diode Characteristics and Maximum Ratings						
I_s	Maximum Continuous Drain–Source Diode Forward Current				-0.8	A
V_{SD}	Drain–Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_s = -0.8 \text{ A}$ (Note 2)		0.77	-1.2	V

Typical Characteristics

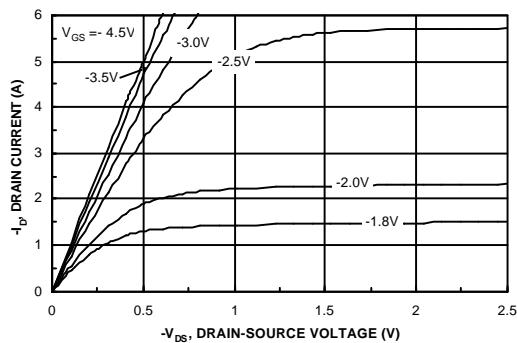


Figure 1. On-Region Characteristics.

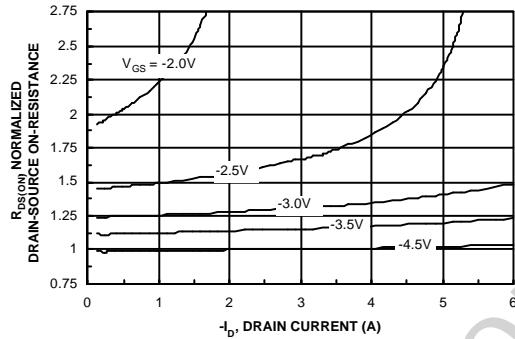


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

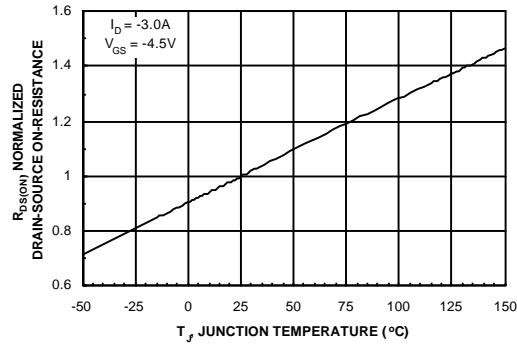


Figure 3. On-Resistance Variation with Temperature.

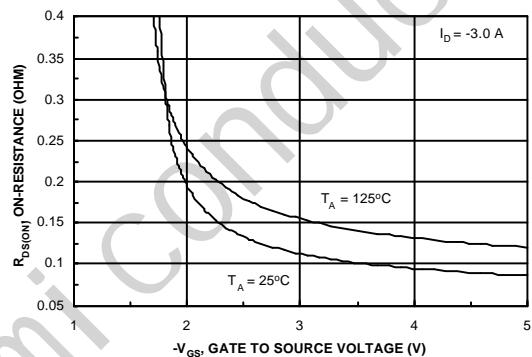


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

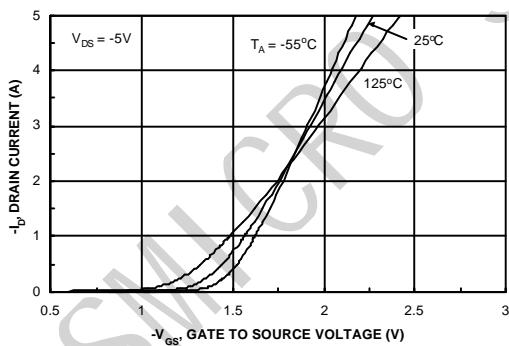


Figure 5. Transfer Characteristics.

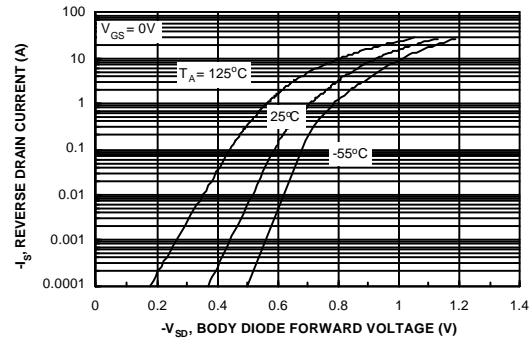


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

Typical Characteristics

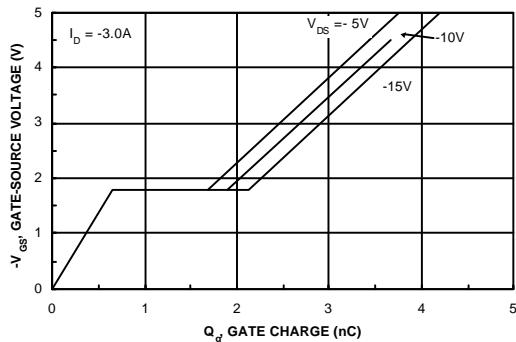


Figure 7. Gate Charge Characteristics.

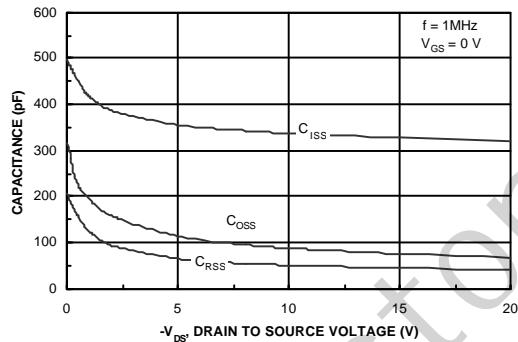


Figure 8. Capacitance Characteristics.

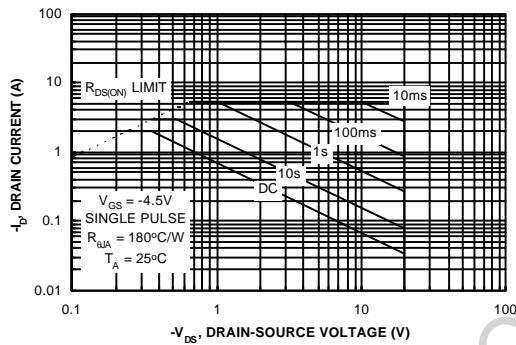


Figure 9. Maximum Safe Operating Area.

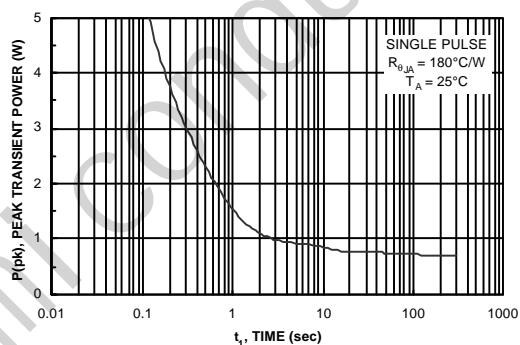


Figure 10. Single Pulse Maximum Power Dissipation.

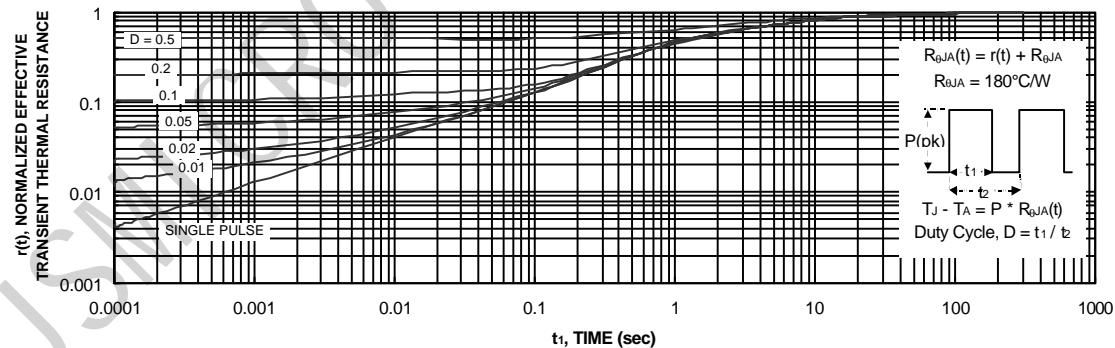
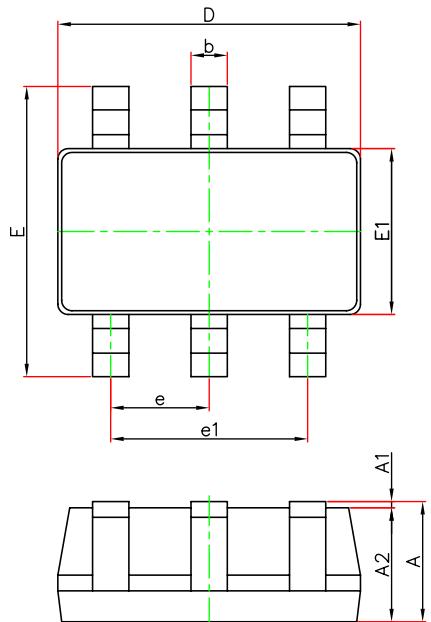
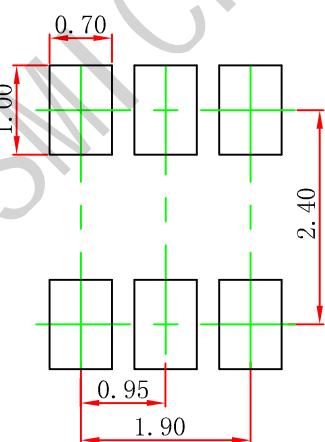


Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1c.
 Transient thermal response will change depending on the circuit board design.

SOT-23-6L Package Outline Dimensions


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

SOT-23-6L Suggested Pad Layout

Note:

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.